

**EAST Search History****EAST Search History (Prior Art)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S113	1941	(multiple plural two) adj2 (shift adj register sr) same clock	US-PGPUB; USPAT	OR	ON	2010/03/19 23:29
S114	875	(multiple plural two) adj2 (shift adj register sr) with clock	US-PGPUB; USPAT	OR	ON	2010/03/19 23:29
S115	863	(multiple plural two) adj2 (shift adj register) with clock	US-PGPUB; USPAT	OR	ON	2010/03/19 23:29
S116	1	(multiple plural two) adj2 (shift adj register) with shift adj rate with clock	US-PGPUB; USPAT	OR	ON	2010/03/19 23:30
S117	7	(multiple plural two) adj2 (shift adj register) with shift\$3 adj rate	US-PGPUB; USPAT	OR	ON	2010/03/19 23:31
S118	18	(FIR ffe dfe) with network with summing	US-PGPUB; USPAT	OR	ON	2010/03/19 23:50
S119	3	(FIR ffe dfe) with network with summing same shift adj register	US-PGPUB; USPAT	OR	ON	2010/03/19 23:50
S120	10	(FIR ffe dfe) with network same shift adj register	US-PGPUB; USPAT	OR	ON	2010/03/19 23:58
S121	7	S120 not S119	US-PGPUB; USPAT	OR	ON	2010/03/19 23:58
S125	3655	(feed adj forward feed-forward feedback feed-back) adj equalizer	US-PGPUB; USPAT	OR	ON	2010/05/08 15:28

S126	612	S125 and shift adj register	US-PGPUB; USPAT	OR	ON	2010/05/08 15:29
S127	24	S125 and shift adj register same (odd and even)	US-PGPUB; USPAT	OR	ON	2010/05/08 15:29
S128	2477	decision adj feedback adj equalizer and dfe	US-PGPUB; USPAT	OR	ON	2010/05/08 18:00
S129	446	S128 and shift adj register	US-PGPUB; USPAT	OR	ON	2010/05/08 18:00
S130	22	S128 and shift adj register same (odd and even)	US-PGPUB; USPAT	OR	ON	2010/05/08 18:00
S131	173	conventional with (decision adj feedback adj equalizer and dfe)	US-PGPUB; USPAT	OR	ON	2010/05/08 18:01

**EAST Search History (I interference)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S122	4104	375/229-236,350.ccls.	USPAT; UPAD	OR	ON	2010/03/20 00:06
S123	3	S122 and (latch and (multiplexer mux) and clock and phase and delay).dm.	USPAT; UPAD	OR	ON	2010/03/20 00:07
S124	0	S122 and (latch and (multiplexer mux) and clock and phase with delay).dm.	USPAT; UPAD	OR	ON	2010/03/20 00:08

5/ 8/ 2010 7:19:20 PM

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